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TRANSMITTAL LETTER
(General - Patent Issued)Docket No.
END920030086US1

Patentee(s): Ng et al.

U.S. Patent No.

7,064,027

Issue Date

June 20, 2006

Title: **METHOD AND STRUCTURE TO USE AN ETCH RESISTANT LINER ON A TRANSISTOR GATE
STRUCTURE TO ACHIEVE HIGH DEVICE PERFORMANCE**COMMISSIONER FOR PATENTS:

Transmitted herewith is:

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JUL 19 2006
of Correction

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Dated: 07-12-2006

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CERTIFICATE OF CORRECTION

PATENT NO : 7,064,027

DATED : June 20, 2006

INVENTOR(S) : Ng et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8

Line 12, delete "niethod" and insert -- method --

Line 64, delete "claim 13" and insert -- claim 11 --

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PATENT NO. 7,064,027

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